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Article

Applicability and Assessment of Diverse FPGA Architectures and Algorithms for the Internet of Things Applications

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Abstract: The "Web of things" (IoT) is turning into an interesting issue in the work environment and outside. IoT is an idea that possibly influences the way of human's life and work area. The IoT can coordinate the actual humankind straightforwardly into PC based frameworks. It additionally permits objects to be detected or controlled from a distance across the current organization framework. IoT further develops proficiency, precision, and monetary advantage of the habit framework to diminish human creation. The test of planning the design in IoT applications is to contract with a copious amount of finding the information produced from the connected appliances that are limited to resources and likely to misplaced information due to the distress in network connectivity. Applications of IoT in the FPGA (Field Programmable Gate Array) framework definitely said to be a challenging one to reach diversity in the upcoming years with the local area networks. FPGA configuration enables a sturdy, less cost architecture that are easy to understand supporting the way of 24 hours of frequent checking with remote detection framework. This paper proposes a review of carrying out the FPGA innovation for IoT applications by investigating different designs. The main aim of this review work is to facilitate the way in which the users can make understand the FPGA-oriented design and architectures embedded with IoT for variety of applications.

Keywords: Internet of Things (IoT), Field Programmable Gate Array (FPGA), Network Infrastructure, Smart Devices, Remote Sensing Systems, Real-Time Monitoring

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1. Introduction

Basics of Web of Things (IoT)

The Web of Things or Internet of Things is an arrangement of linked actual objects or gadgets available via the Internet. The term 'thing' in IoT might refer to any individual with a basic monitor or a vehicle that are assembled with sensors, i.e., gadgets that are assigned with an Internet Protocol (IP) address that can assemble and share the information over an organization without any manual intervension or mediation. The implanted technology inside these gadgets/objects assists them by communicating with interior states or the outside climate, which influences the choices taken. IoT interfaces several devices inserted to the fundamental server web. Whenever gadgets/articles can address themselves carefully, they can be controlled anyplace by anyone, as introduced in Figure 1. This network further helps us with gathering additional information from

additional spots, guaranteeing numerous ways of improving effectiveness and further develop safety and security.

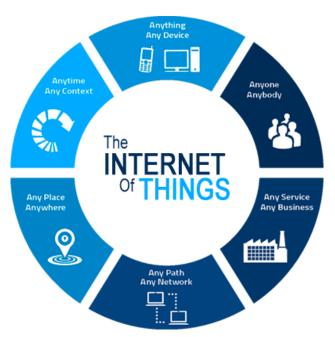


Figure 1. Concept of Internet of Things (IoT).

IoT can be referred as a groundbreaking energy that could assist the segment with working on its presentation throughout the IoT investigation and security to convey improved outcomes. Some application regions are organizations in the utilities, oil and gas, protection, production, transportation, framework, and retail areas. Each one of these can work on the advantages of IoT by assembling it with greater knowledgeable choices, with the progression of interactivity and value-based data as a supporting factor. This new availability strategy goes past workstations and advanced mobile phones towards connected cars, smart homes, associated wearables, shrewd urban communities, and associated healthcare [1], [2]. Which can be mentioned by the term connected life. As per the Gartner statement, illustrated in Table 1, near to the year 2025, the associated gadgets across the innovations might arrive at 20.6 billion. VLSI technology plays a vital role in development of Internet of things (IoT) for a wide range of applications like agriculture, industrial automation, smart cities, health care, traffic control and so on [3],[4],[5],[6], [7].

Table 1. Approximate number of devices connected (present and in future).

Year	Number of connected devices		
1989	0.28 million		
1998	89.0 million		
2012	6.0 billion		
2014	10.0 billion		
2025	1.02 trillion		

System architecture of IoT applications

Internet of Things is worldwide milieus in which the regular gadgets turn out to be more smart, handling of devices grow to be savvy, and transmission gets more instructive. A short review of P.P.Ray portrays the IoT-established system designs as competent and

efficient to work on the comprehension of connected tools, innovation, and strategy to work with the designer's prerequisite [8]. The review depicts the latest things in engineering to inspire educational and production purposes to engage in the specific influence of IoT.

Design of IoT framework incorporates four phases, in particular:

- Stage 1: comprises of organized things, for instance remote sensors and actuators.
- Stage 2: incorporates the major details like sensor information, aggregation systems, and analog-to-digital conversions.
- Stage 3: tends to limit the IT frameworks to execute the information pre-processing prior to moving to the data to the server farm or the cloud.
- Stage 4: then the processed information is analyzed, managed, and stored on conservative client-facing server farm structures.

Problem Statement

Internet of Things (IoT) will comprise of billions of gadgets in no time. There have been expectations that, continuously 2020, around 8 billion gadgets will have connectivity to the Internet. This technology's deployment will require changes to the ongoing organization framework, protocols and services. It can likewise be conveyed by empowering the VLSI [9], [10] plan or a VHDL code; the precise representation is implied, as displayed in Figure 2. The making of compound applications that are important for various enterprises can be worked with utilizing IoT [11],[12]. These applications will be the part of everyday life existence, and their effect on our general public is critical. The assumptions for income amazing open doors are around \$ 2.7 trillion. Many issues, for example, no standardized architecture, security, safety concerns, and interoperability between merchants, plague IoT. Henceforth, our work is to execute the innovation worldview for IoT on FPGA. Utilizing FPGA models gives improved results in light of its benefits like minimal expense, less chance to advertise, re-programmability, and lesser area utilization of about 28nm CMOS plan innovation.

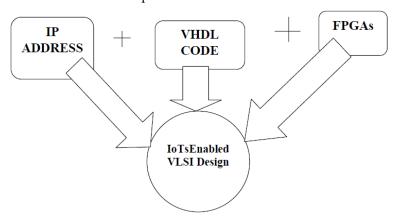


Figure 2. VLSI design by enabled with IoT.

Role of FPGA in IoT Applications

FPGA based devices are predictable to take over the IoT based environment in next few years. The connected gadgets can undoubtedly communicate with rest of the world to enjoy the benefit of giving the most reduced power, least latency, and best acceptance rate. Sensors with IoT features can be connected to measure temperature and humidity, current and voltage, vehicle or automobile acceleration, location, force, analog-to-digital converters (ADCs), digital-to-analog converters (DACs), and other assorted purposes [13]. The overall idea of re-organization of FPGA-based web administration is exemplified in Figure 3.

FPGA is integrated with Acron RISC Machine (ARM) processors [14], [15], [16] to utilize the most essential programming capabilities like web servers or protection packages assuming that a more elevated level of handling is required. The significant idea in developing is the programmable element of an FPGA platform. A developer advancement kit is expected to configure FPGA in fostering the IoT environment cycle. All the while, as required, a Printed Circuit Board (PCB) is designed including explicit sensor/correspondence/ and some output components like displays.

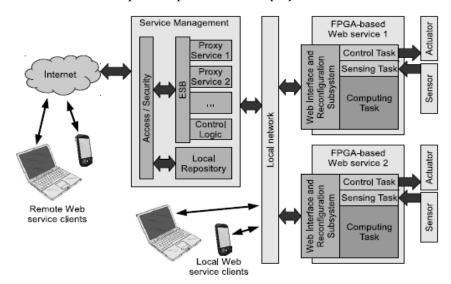


Figure 3. Reconfigurable FPGA-based web service.

Disputes and design concern

The disputes in IoT design incorporates protection, unapproved access, unauthorized organizations, and contradiction of administration. Developer should overcome huge execution difficulties to arrive at billions or trillions of connected gadgets to form the IoT cloud. Handling huge data generated by various sensors is difficult for any VLSI system to deploy update technologies, in order to handle that ontology based data compression has been produced by various research works [17], [18], [19], [20], [21], [22]. Among them, a portion of the keys will make IoT gadgets power-proficient, dealing with incompatible connection points, and providing a processing development way to deal with the inevitable expansion in performance requirements of the connected appliances. A FPGA-oriented system design can assist with solving the above mentioned discrepancies.

2. Materials and Methods

A basic least-intricacy FPGA accomplishment of multi-spectral and Hyper-spectral Lossless Compressor (HyLoC) for some applications in space has been introduced by Santos et al. [23]. In current and future space missions, effective compression of hyper-spectral pictures locally available satellites is obligatory to save data transfer capacity and least storage area. Lessening the volume of information in space is identified to be a major challenge faced in two-phased methodology [24], [25]. This work aims to propose a novel, higher productive compression calculations and produce innovations and make the systems to perform the compression in locally accessible equipment.

Hardware architecture is considered and intended to accomplish a least hardware inhabitance and greater execution on a space-qualified application of FPGA system from the miniature unit. This uses the Consultative Committee for Space Data Systems (CCSDS123) calculation is illustrated in the stream chart in Figure 4. The subsequent FPGA execution is, in this way, reasonable for installed compression. The proposed execution

benefits are additionally confirmed by a demonstrator carried out on a viable prototype (i.e., PCB board) through Xilinx tool. In conclusion, an examination among other FPGA kits are executed using locally available data compression approaches were presented [26], [27], [28].

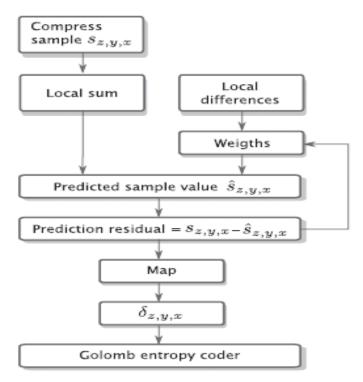


Figure 4. Stream chart of CCSDS123 algorithm.

Anitha et al. [29], [30], [31] have established Very Large Scale Integration (VLSI) based system design with a clock-gated turbo encoder for wireless sensor networks [32], [33] . This work is carried out through establishing a clock gating strategy to decrease power utilization, as illustrated in Figure 5. This architecture utilizes an essential Add Compare Select (ACS) activity. Parallel processing activity of ACS blocks leads to low handling steps that consequently leading to a least transmission energy with lesser design complexity as well. This research work execution implements a high throughput, less memory prerequisite, diminished complexity, and decreased power utilization.

Velu et al. [31] have proposed an Orthogonal Frequency-Division Multiple Access (OFDMA) way of encoding and decoding method for Network Operations Center (NOC) in Multi-Input and Multiple-Output (MIMO) architecture to improve the speed and efficiency of communication. The proposed calculation is an interesting strategy for the stage to sine sufficiency transformation, which gives quick and region effective calculations of mathematical functions namely sine and cosine. The developed CORDIC implementation involves pipeline processing to support the proceeded data path. Through utilizing pipeline engineering, the proposed design can compute consistent input results in higher throughput which needn't bother with memory or temporary registers in order to save the angle iteration.

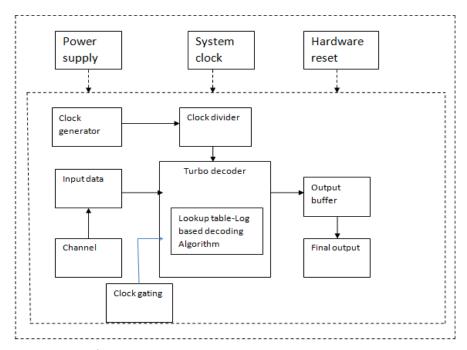


Figure 5. Clock-gating turbo decoder architecture.

The Fiber-Wireless Sensor Networks (Fi-WSNs) that are made out of a fusion Fiber-Wireless (FiWi) networks improved with sensors will assume a critical part in sustaining the Machine-to-Machine (M2M) correspondences. This combination helps to empower a wide scope of Internet of Things (IoT) appliances, through which the smart frameworks address a significant real-world model.

Chances of designing an energy-proficient Fi-WSN in view of Ethernet Passive Optical Network (EPON)/10G-EPON, Wireless Local Area Network (WLAN), remote sensors, and aloof fiber based optic sensors are insisting. The implementation is considered as common interchanges foundation for wider bandwidth administrations and smart equipments that has been investigated by Van et al. [34]. The Internet of Things (IoT) is a dynamic, ever developing "living" element. Henceforth, in present days the FPGA-oriented smart gadgets embedded with Dynamic Partial Reconfiguration (DPR) abilities, permits in-field harmless changes for the electronic circuit executed on the FPGA [37]. Typically, the enactment of proposed DPR abilities involves extra licenses from the FPGA merchant, as illustrated in Figure 6.

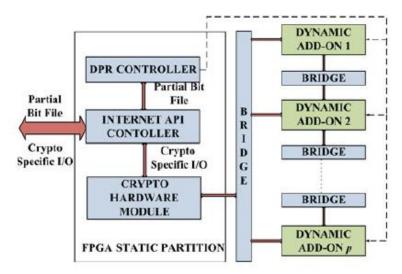


Figure 6. The architecture of proposed FPGA system with enabled-DPR.

The customized DPR [35] [36], [38], [39] approach doesn't need any additional "add-on" implementation to carry out a insubstantial cryptographic protection protocols. The proposed work depicts the benefit of the DPR capacities in FPGA over the IoT applications. This additionally investigates the potential dangers that could appear due to DPR accessibility on the IoT nodes. This work also includes a few potential arrangements in view of Physically Unclonable Function (PUF) electronic circuits to forestall such dangers. Cognitive radios (CR) empower productive range used for evaluating range detecting to figure out current range inhabitance and progressively reconfiguring the correspondence boundaries, making them profoundly appropriate for IoT frameworks.

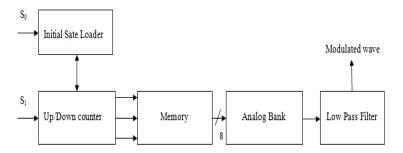


Figure 7. The architecture of low complex QPSK modulator.

Kiran et al. [40] proposes a QPSK-based transmitter architecture with a low complex circuit design avoiding the mixer component, as illustrated in Figure 7. A prototype model is created in this work utilizing FPGA as the baseband regulator and Bipolar Junction Transistors (BJTs) as a main component. This developed architecture handily adjusted to MOSFET innovation, whereas the regulation has been accomplished exclusive of producing the transporter remotely. This prototype model was effectively experimented by producing input frequencies of reach shifting from 1 kHz - to 120 MHz. This methodology can likewise be utilized for some other advanced regulation plan, for example, Frequency Shift Key (FSK), Binary Phase Shift Key (BPSK), and so on.

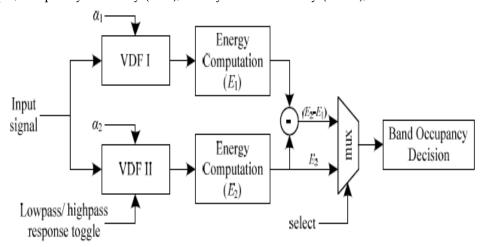


Figure 8. Block diagram of the VDF-based spectrum sensing scheme.

Ambede et al. [41] has developed Variable Digital Filter (VDF) oriented equipment executed with a range detecting plan for CR (Cognitive Radio) grounded IoT framework applications which involves huge transmission capacity. This developed prototype model is evaluated on FPGA, as illustrated in Figure 8. In general the proposed prototype accomplishes superior execution with less resource consumption and at partially greater power utilization [42], [43].

In the proposed scheme, two VDFs giving unedited command above the cutoff recurrence were utilized to figure power requirement in comparison with all the three frequency bandwidth cases. The productive experimentation utilizes the range detecting plan and gives equipment execution models to the VFDs on various FPGA kits with different functionalities [44], [45], [46], [47], [48], [49], [50]. FPGAs are ultimate contender for Cognitive Radios. FPGAs permit compound calculations to increase the processing speed of an equipment and deal methods to powerfully adjust equipment units and helps multi-standard cognitive radio or proficient molding/range detecting procedures.

3. Results and Discussion

The proposed methodology for designing FPGA architecture for IoT applications heads toward the goals like designing and developing an FPGA-embedded IoT execution for multi-layered sensor nodes and control systems. They are using low-cost FPGA to implement the overall IoT subset, as well as control systems, data acquisition, etc. Manifestation of a simple FPGA kit containing and controlling a number of IOT implementations simultaneously is considered. The proposed design develops a high-level VHDL program in order to make possible to the multi-platform portability for IoT based implementations.

This paper reviews the current FPGA architectures and algorithms developed for the IoT domain till the year 2020. This manuscript identifies various proposed new architectures' design principles and advantages from other architectures' shortcomings. Various design issues in the FPGA platform for IoT applications, such as security issues, performance and interoperability have been addressed in this manuscript. The comparison study of few essential parameters and characteristics of the proposed FPGA architectures are illustrated in Table 2.

FPGAs are used to bridge the gap between software and hardware by offering a number of advantages like:

- a. Low cost
- b. Flexibility
- c. Fast time-to-market
- d. Long-term maintenance and
- e. Reliability.

Keeping in account of all the approaches and architectures discussed above for review, it can be accomplished that the FPGA is a best reconfigurable system hardware for implementing IoT based applications. By comparing various algorithms from Table II, it can be concluded that when less storage is recommended, clock-gating BCJR of about 128.8kb, CCSDS123, and DPR-based approach is about 150kb is said to be efficient. While designing the FPGA architecture for IoT, if the cost is considered, then CORDIC and DPR approaches will give better results at the same time for high-frequency operations; the Nb-IoT-based FPGA design is said to be more efficient since its operating frequency ranges from 750MHz to 960MHz. The Eco-SFiWi-based design is said to have less power consumption of about 120mW; similarly, algorithms like CCSDS123, DPR, and CORDIC algorithm also support low power consumption IoT applications. CCSDS123 algorithm and the clock-gating BCJR algorithms have less design complexity FPGA of about 74% and 71%, respectively.

Table 2. Comparative analysis of various FPGA architectures proposed for IoT applications.

-		Parameters Measured					
S. No	Name of the architecture/ approach	Complexity	Power consumption	Operating frequency	Cost	Storage/ memory space	
1	CCSDS123	Low	High	-	H/W cost is	Lesser	
	[17]		(>400mW)		high		
2	CORDIC	Medium	Reduced	199.28MHz	Less	No need	
	[19]	74%				for	
						external	
						memory	
3	Clockgating BCJR [18]	Less 71%	Reduced by 32%	120MHz	-	128.8kb	
4	DPR [24]	-	Low	180MHz	Low	150kb	
					maintenance		
					cost		
5	Eco-SFiWi [23]	High 86%	Low (120mW)	-	Less	320kb	
6	QPSK based	Medium	Less	1KHz-	Low	_	
Ü	[26]	76%	200	120MHz	2011		
7	VDF [29]	Medium	High (269mW)	100MHz	High	Large	
	. [.]	76%	8 (3 7 7)		implementati	memory	
					on cost	capacity	
8	NB-IoT	High 84%	Very low	750-	High	low	
	based [14]	O	J	960MHz	maintenance		
	. 1				cost		

It can be understood that algorithms such as CORDIC, CCSDS123, and DPR-based approaches are more efficient, with low complexity of about 71%, lesser power consumption of about 120mW, and can be operated at a high-frequency rate ranges about 750MHz-960MHz when compared to the convolution algorithms.

4. Conclusion

Implementing FPGA architecture on the IoT platform is a more efficient and userfriendly approach for real-time applications. The expertises in core infrastructure layers in day-to-day world were showing good signs of development. In future work, we consider the BCJR approach because of its low complexity of about 71% and less storage space of about 128.8kb, and reduced power consumption by 32% along with Nb-IoT based approach due to its high operating frequency of about 750MHz-960MHz and CORDIC architecture because of its reduced power consumption, less cost, 74% complexity and operating frequency of about 199.28MHz. Considering all the above advantages, a new FPGA approach will be designed for IoT applications. IoT has been finished up as the normal eventual future of Network/Internet. IoT associates billions and trillions of gadgets and empowers M2M correspondence. This could change day to day existence with ordinary objects associated to the Internet connectivity. As of now, this field is in an exceptionally nacent stage. In any case, significantly an added requirement has to arise in IoT based applications and correspondence technologies. These fields tends to develop and influence human existence in incomprehensible ways over the course of the following 10 years.

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